

1/10

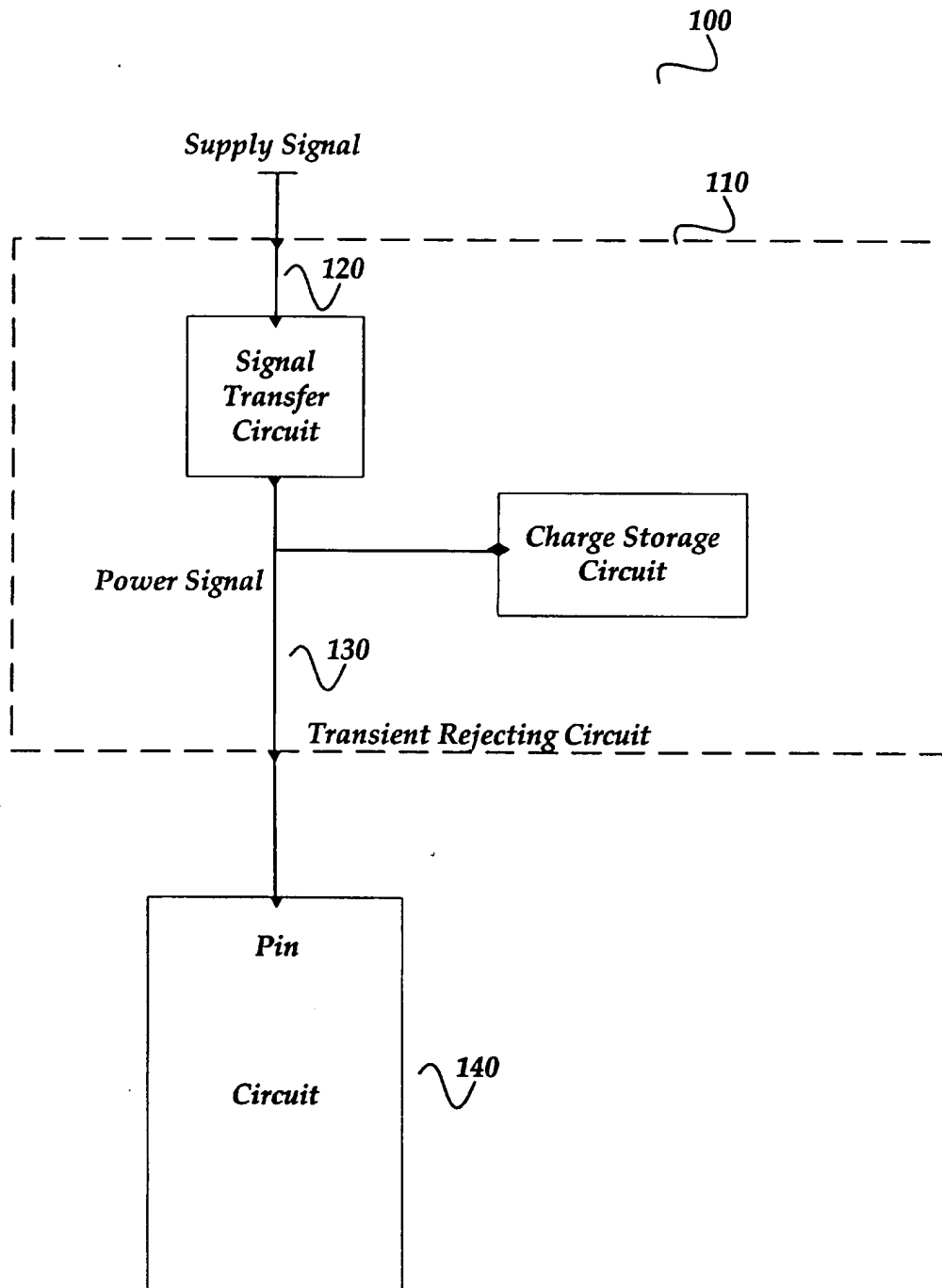


Fig.1

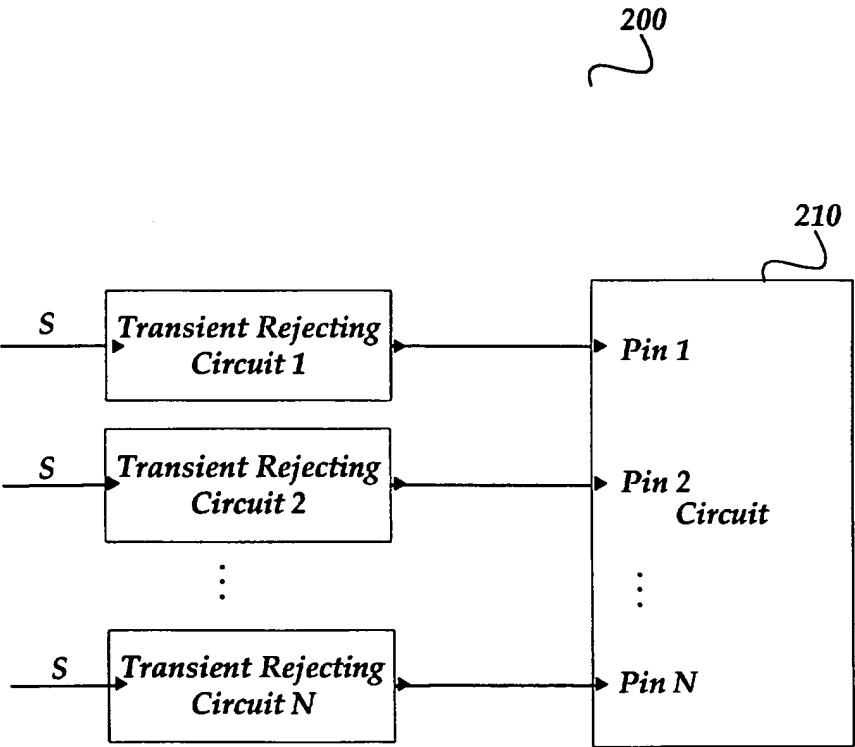


Fig.2

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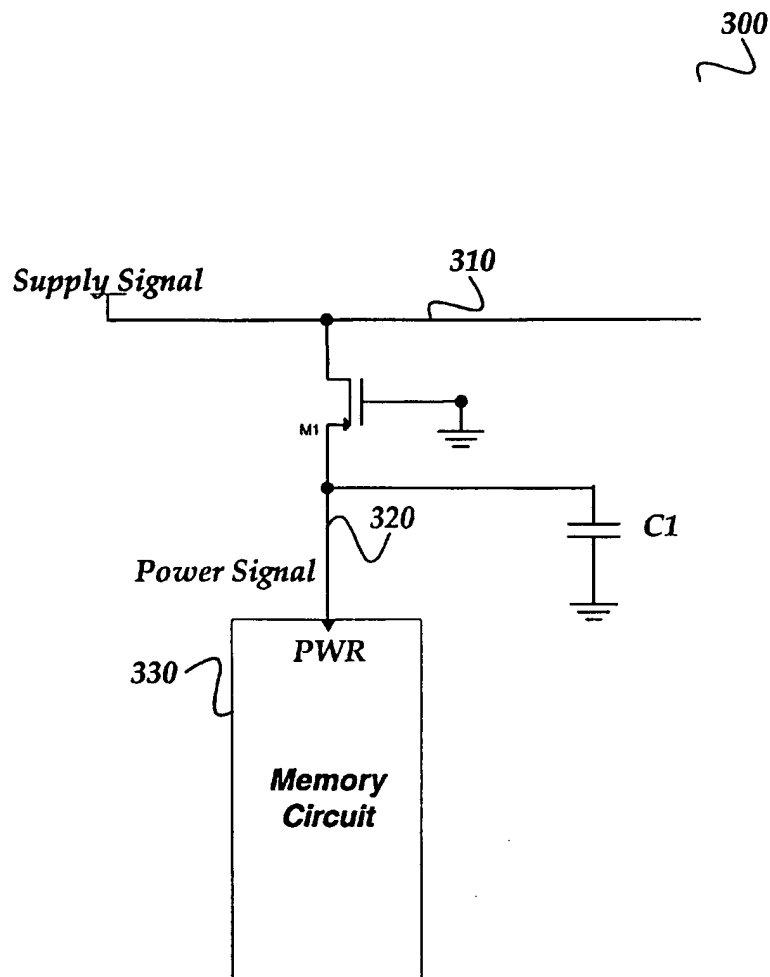


Fig.3

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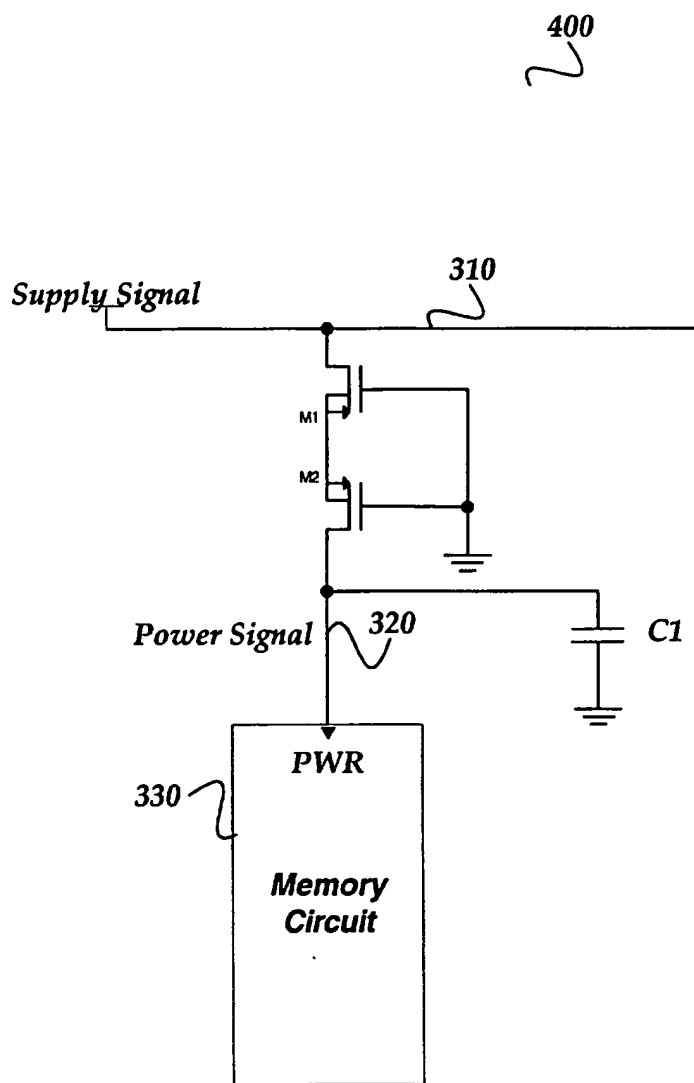


Fig.4

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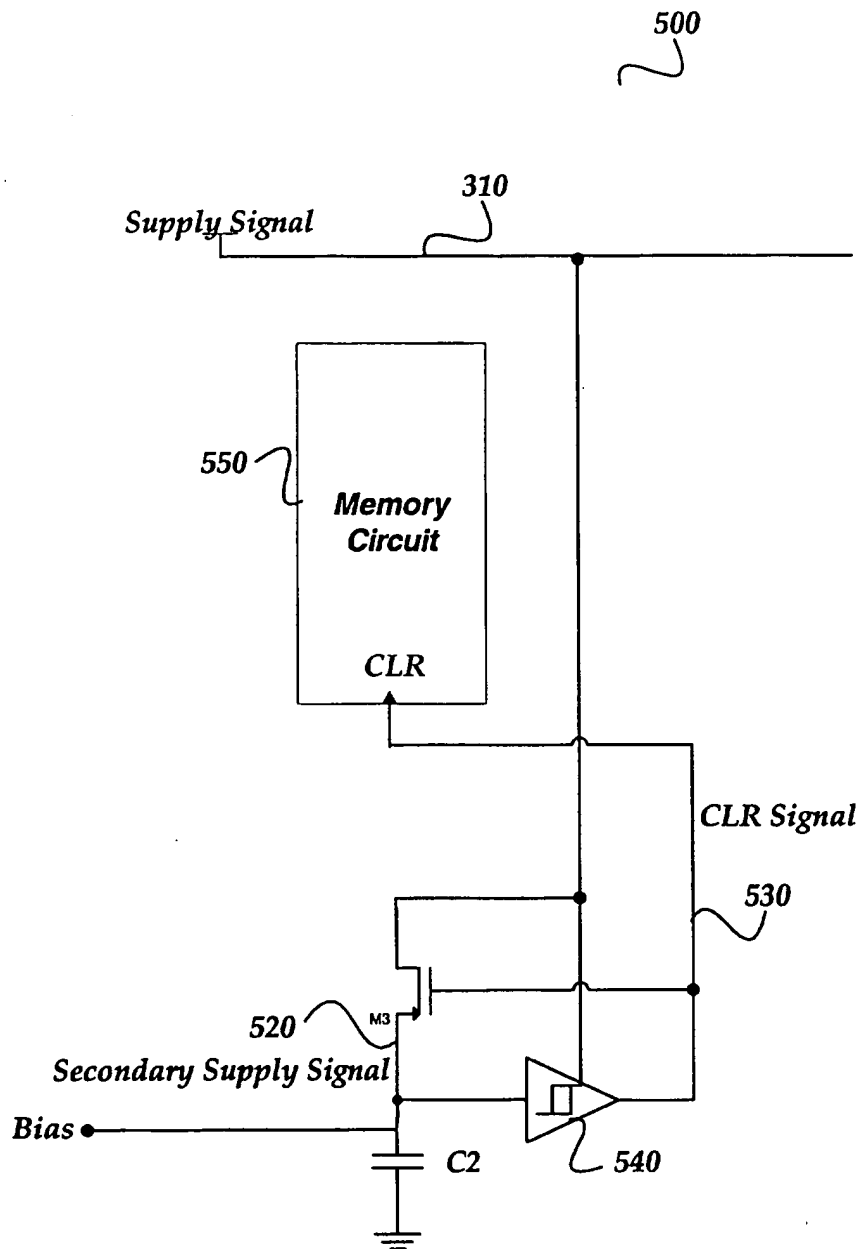


Fig.5

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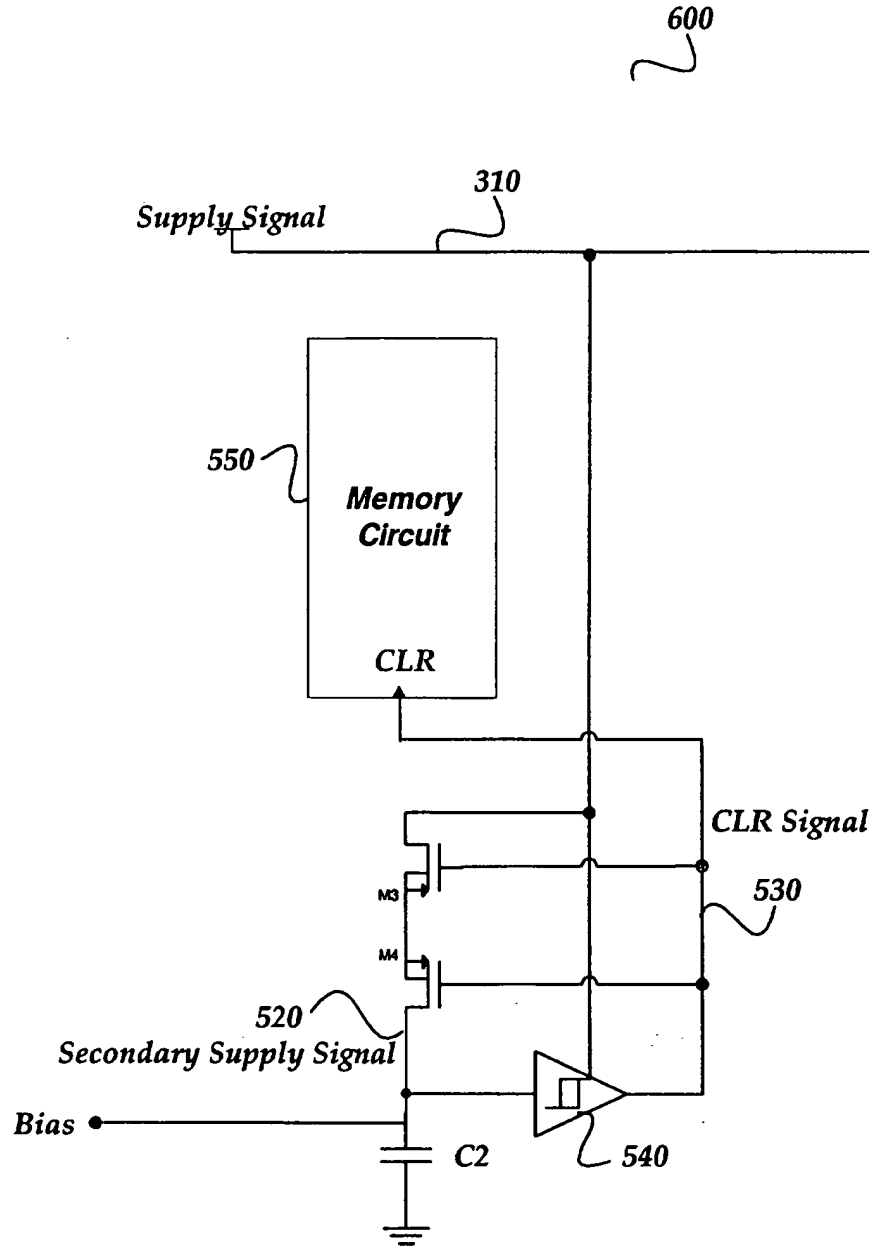


Fig.6

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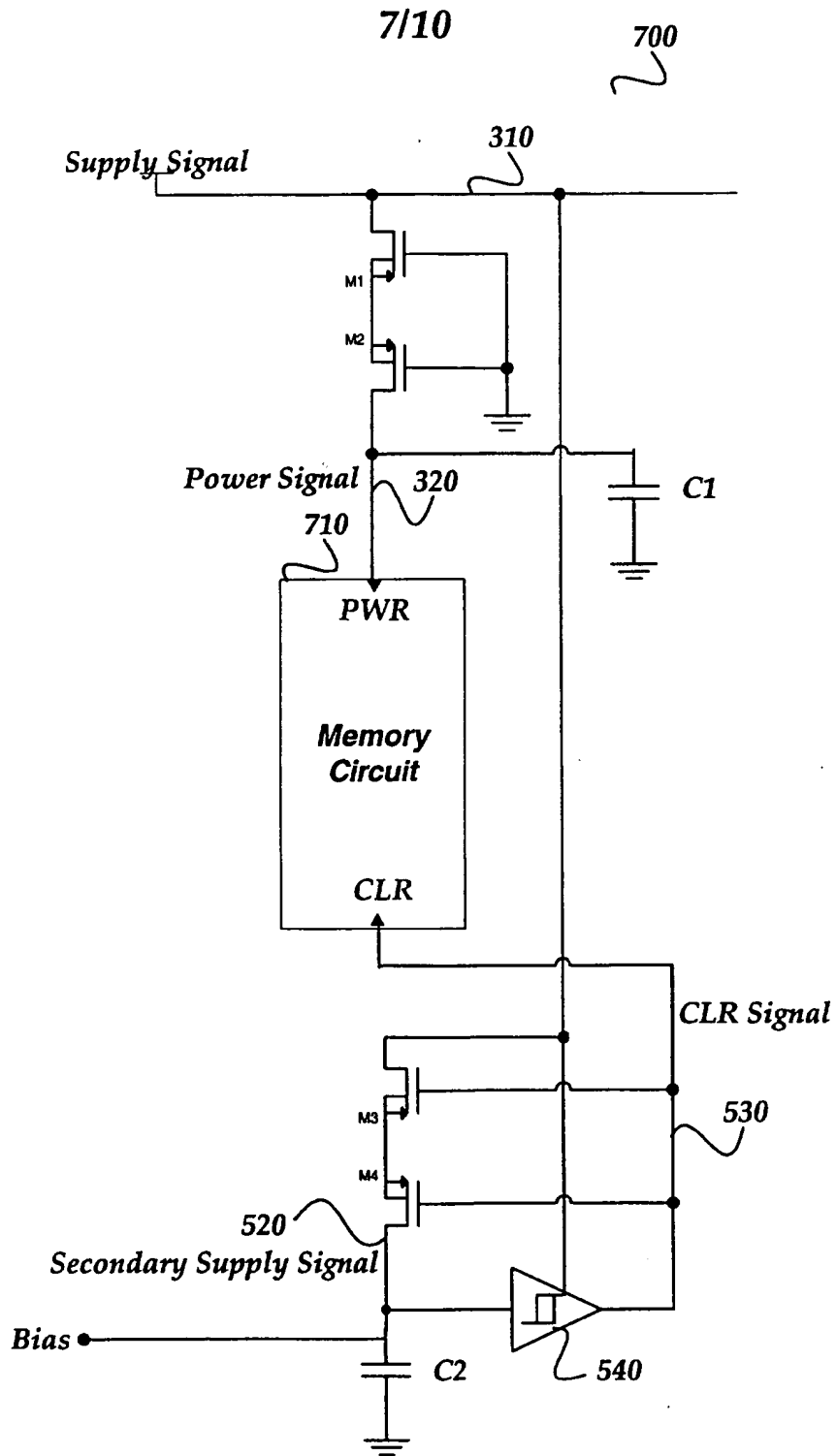


Fig.7

FIG. 7 is a schematic diagram of a transient rejecting circuit.

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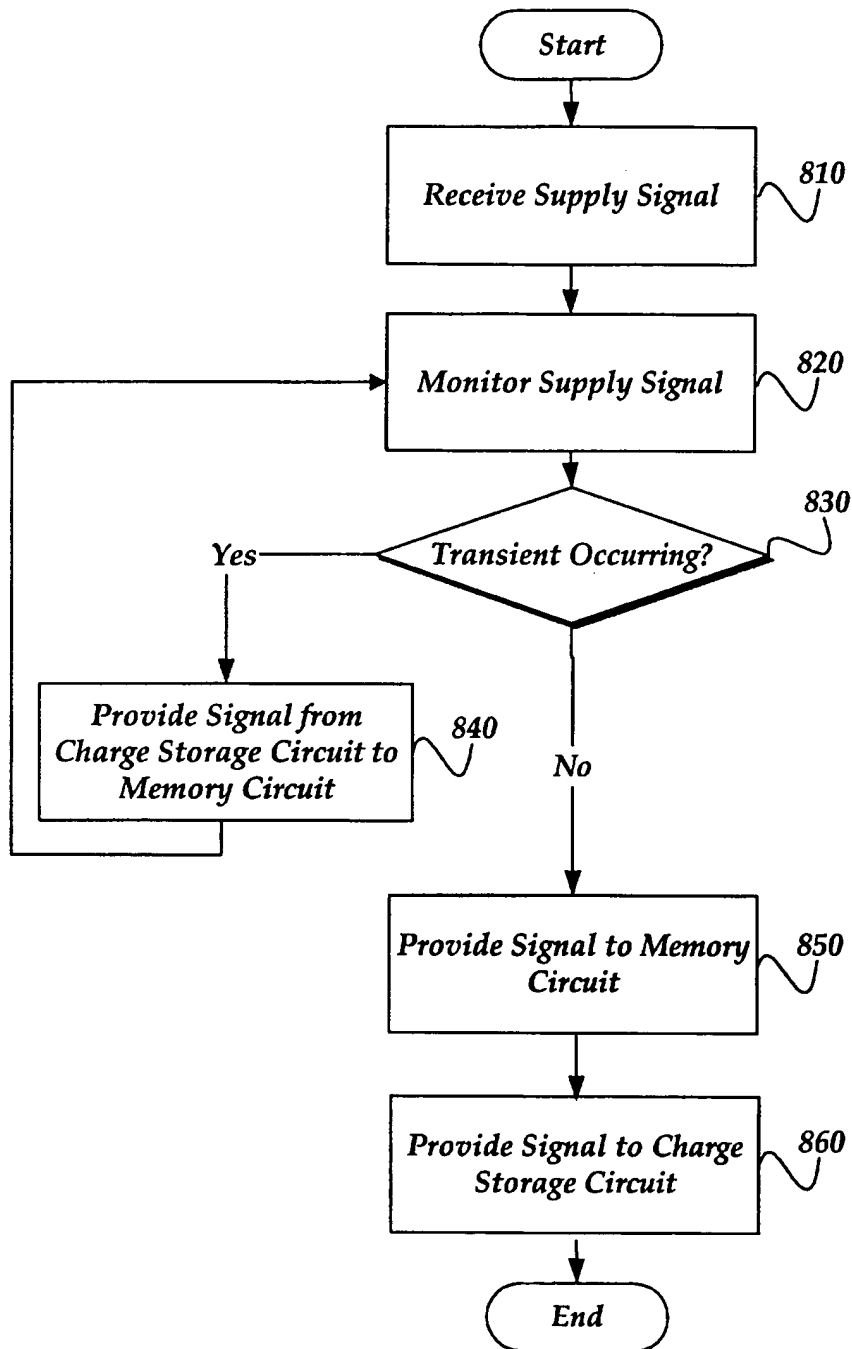


Fig.8



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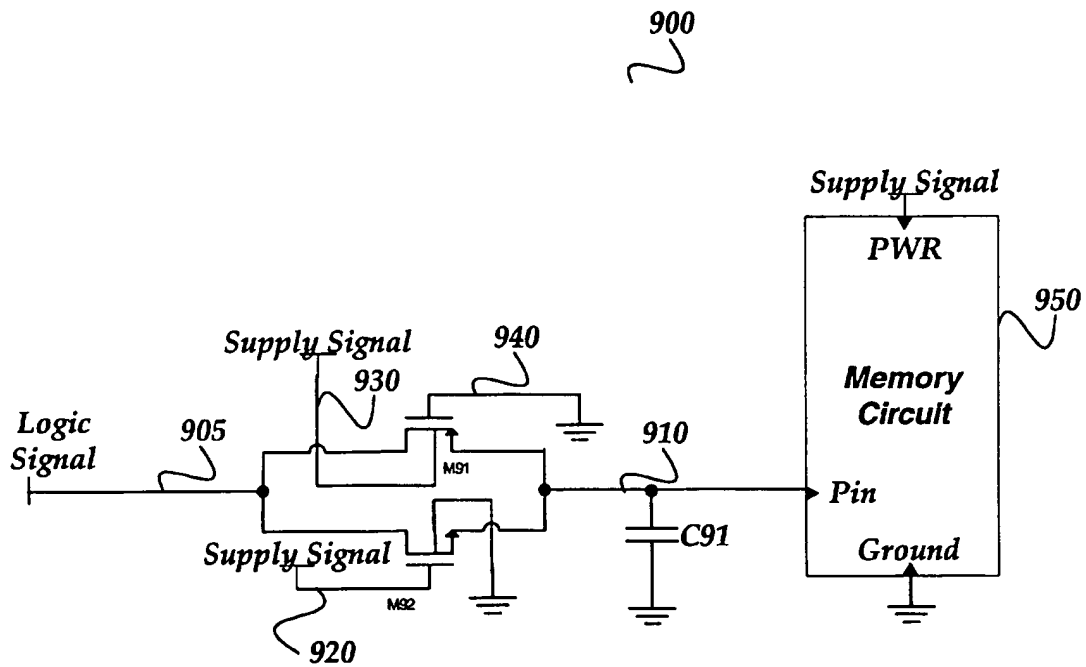


Fig.9

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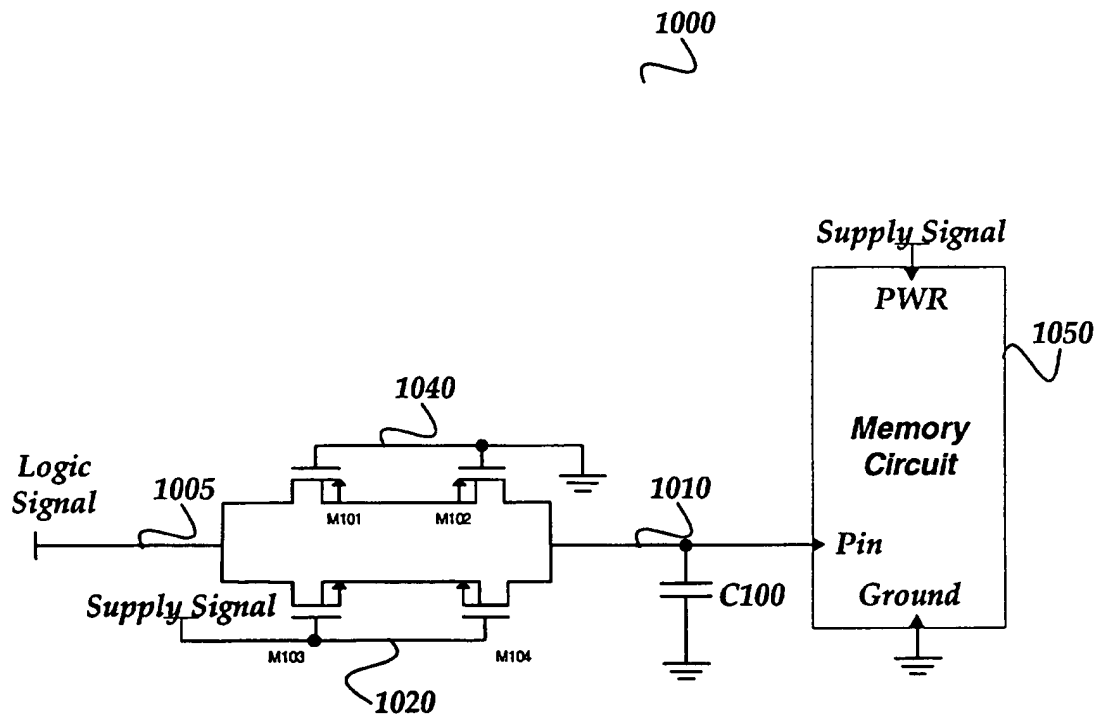


Fig.10